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U.S. **UTILITY** Patent Application

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| <p>O.I.P.E.</p> <p>SCANNED  Q.A. </p> | <p>PATENT DATE</p> |
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| APPLICATION NO. 09/933492 | CONT/PRIOR D | CLASS 257 458 | SUBCLASS 75D | ART UNIT 2815 2812 | EXAMINER CHU |
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APPLICANTS


David Hembree
Alan Wood

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TITLE

Method for fabricating semiconductor components

PTO-2040
12/99[illegible]

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|---|--|-------------|-------------|-----------------------------------|-----------------------|
|  TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drawn | Figs. Drawn | Print Figs. | Total Claims | Print Claims for O.G. |
| <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
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| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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